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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10074840	FILING DATE 02/13/2002	CLASS 438	SUBCLASS 2	GAU 2827	EXAMINER Z. J. [Signature]
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**APPLICANTS: Farnsworth Warren;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/640,801 08/17/2000, now US PAT 6432752

[Signature]

NOTE: DISCLAIMER
The term of this patent shall not extend
beyond the expiration date
of Pat. No. 6432752

** FOREIGN APPLICATIONS VERIFIED:

[Signature]

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO 4212.3 (99-0306.3)	
Verified and Acknowledged Examiner's initials [Signature]			

TITLE : Stereolithographic methods for fabricating hermetic semiconductor device packages and semiconductor devices including stereolithographically fabricated hermetic packages

U.S. DEPT. OF COMM /PAT. & TM-PTO-43EL (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED 10/11/02		CLAIMS ALLOWED Davida. Janette Assistant Examiner	
ISSUE FEE Amount Due \$1580		Total Claims 26 Print Claim for O.G	
TERMINAL DISCLAIMER		DRAWING Sheets Drawn 9 Fig. Drawn 23 Print Fig. 1	
Primary Examiner Albert W. PALADINI PRIMARY EXAMINER 9-70-02		Application Examiner 10/2/02	
PREPARED FOR ISSUE			
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